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Statement

Over the last few years, IEEE top leadership lost its focus on membership, emphasizing corporate mandates instead of member value, while accumulating multimillion dollar operational deficits. The IEEE Board holds long closed sessions, Board Directors are instructed to keep these in utmost secrecy. As President, I will strive for an open organization addressing two persistent IEEE challenges: dramatically increase IEEE membership through enhanced member value, and address the many multimillion dollar deficit of IEEE's operational budget. Our membership is aging and many young professionals do not find value in membership, yet, they seek networking opportunities more than ever. As President, with volunteers and staff from Regions, Societies, and Councils, I will leverage technology to attract new members, develop new membership models, new products and services that provide value to our members in different regions, different age groups and gender, and different industry segments. With volunteers and staff, I will develop a transparent financial system that explains where each dollar is spent, roots out waste, promotes efficiency, and reduces costs without affecting the quality of products and services to you, our members. With your vote, we will bring the best of the IEEE to the next level! Visit www.josemoura.com for details.

IEEE selected activities

IEEE Boards: IEEE Board of Directors: Vice-President (VP) Technical Activities Board, Officer, and Director (2016); Member and Division IX Director (2013-14); Awards Board (2012-14); Educational Activities Board (2010); Publication Services and Products Board (PSPB): Member (Vice-Chair 2008, 2011-12 (2008-09, 2011-12)); PSPB Strategic Planning Committee (2008-09, 2011-12); IEEE Proceedings (1999-2007). Signal Processing Society: Awards Board Chair (2014-2016); President (2008-09); VP Publications (2000-02); Transactions on Signal Processing Editor-in-Chief (1995-1999). Sensors Council: Founding VP Publications 2000-02. IEEE Portugal Section: Founding Member 1980-84.

Brief bio

José Moura is Chaired University Professor at CMU. Recognitions: member US National Academy of Engineering, corresponding member of Portugal Academy of Sciences, Fellow of US National Academy of Inventors, IEEE Fellow and AAAS Fellow, member of the US National Academies Navy Study Board. Awards: He received from the IEEE Signal Processing Society the Technical Achievement Award and the Society Award for outstanding technical contributions and leadership in Signal Processing. In 2016, he received the CMU Distinguished Professor of Engineering Award. Background: He holds a doctorate from MIT and an EE from IST, Portugal. He was a Professor at IST, visiting Professor at MIT and NYU. Accomplishments: founded a large research program between CMU and Portugal, www.cmuportugal.org; holds 14 patents, two used in 3 billion disk drive chips in 60% of all computers sold in last 12 years (Feb 2016 \$750Million settlement between CMU and chip manufacturer); cofounded Spiralgen www.spiralgen.com; technology licensed by Intel (10 million lines of code in Intel's IPP) and Siemens (MRI scanners).

For more information:



Thank you for your vote!

Learn more about my position statement, qualifications, and IEEE activities at:
<http://www.josemoura.com/>

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